

IN THE SPECIFICATION

Please amend the paragraph under the RELATED APPLICATIONS heading at page 1 lines 4-9 as follows:

This patent application is related to U.S. patent application “METHOD TO PROVIDE ON-DEMAND RESOURCE ACCESS”, serial no. 10/406,652 filed on 04/03/03, and is related to U.S. patent application “APPARATUS AND METHOD FOR PROVIDING METERED CAPACITY OF COMPUTER RESOURCES”, serial no. _____ 10/616,676 filed on _____ 07/10/03. Both of these patent applications are incorporated herein by reference.

Please amend the paragraph at page 3 lines 20-25 as follows:

Another way to provide temporary access to resources is known as “metered capacity on demand”, and is the subject of the related application “APPARATUS AND METHOD FOR PROVIDING METERED CAPACITY OF COMPUTER RESOURCES”, serial no. _____ 10/616,676 filed on _____ 07/10/03. When metered capacity on demand is used, a temporary resource is enabled, and only actual use of the resource is billed to the customer.

Please amend the paragraph at page 6 lines 4-5 as follows:

The preferred embodiments of the present invention will hereinafter be described in conjunction with the appended drawings, where like designations denote like elements, and:

FIG. 1 is a block diagram of a computer apparatus in accordance with the preferred embodiments;

FIG. 2 is a block diagram of the partition manager of FIG. 1;

FIG. 3 is a block diagram showing logical components in a logically partitioned computer system;

FIG. 4 is a flow diagram of a method for providing temporary resources on demand;

FIG. 5 is a flow diagram of a method in accordance with the preferred embodiments for providing temporary resources on demand in a manner that assures the temporary resources may be recovered; [[and]]

FIGS. 6-[[10]] 7 each represent the allocation of processors between three logical partitions to illustrate the principles of the preferred embodiments;

FIG. 8 represents the allocation of processors between three logical partitions to illustrate the principles of the prior art; and

FIGS. 9-10 each represent the allocation of processors between three logical partitions to illustrate the principles of the preferred embodiments.